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| DB=P | GPB,USPT; PLUR=YES; OP=OR | | | | | |
| <u>L32</u> | 14 and 19 | · 5 | <u>L32</u> | | | |
| <u>L31</u> | 14 and 18 | 40 | <u>L31</u> | | | |
| <u>L30</u> | 14 and 17 | 46 | <u>L30</u> | | | |
| <u>L29</u> | 14 and 16 | 74 | <u>L29</u> | | | |
| <u>L28</u> | 14 and 15 | 182 | <u>L28</u> | | | |
| <u>L27</u> | 13 and 110 | 5 | <u>L27</u> | | | |
| <u>L26</u> | 13 and 19 | 5 | <u>L26</u> | | | |
| <u>L25</u> | 13 and 18 | 41 | <u>L25</u> | | | |
| <u>L24</u> | 13 and 17 | 47 | <u>L24</u> | | | |
| <u>L23</u> | 13 and 16 | 75 | <u>L23</u> | | | |
| <u>L22</u> | 13 and 15 | 185 | <u>L22</u> | | | |
| <u>L21</u> | 12 and 110 | 5 | <u>L21</u> | | | |
| <u>L20</u> | 12 and 19 | 5 | <u>L20</u> | | | |
| | | | | | | |

WEST Refine Search Page 2 of 2

| <u>L19</u> | 12 and 18 | 41 | <u>L19</u> |
|------------|---|-------|------------|
| <u>L18</u> | 12 and 17 | 47 | <u>L18</u> |
| <u>L17</u> | 12 and 16 | 75 | <u>L17</u> |
| <u>L16</u> | 12 and 15 | 185 | <u>L16</u> |
| <u>L15</u> | 11 and 19 | 16 | <u>L15</u> |
| <u>L14</u> | 11 and 18 | 138 | <u>L14</u> |
| <u>L13</u> | 11 and 17 | 431 | <u>L13</u> |
| <u>L12</u> | 11 and 16 | 623 | <u>L12</u> |
| <u>L11</u> | 11 and 15 | 1149 | <u>L11</u> |
| <u>L10</u> | (711/203)[CCLS] | 970 | <u>L10</u> |
| <u>L9</u> | (711/203)[CCLS] | 970 | <u>L9</u> |
| <u>L8</u> | (711/201-221)[CCLS] | 6210 | <u>L8</u> |
| <u>L7</u> | (712/239-240)[CCLS] | 652 | <u>L7</u> |
| <u>L6</u> | (712/233-240)[CCLS] | 1670 | <u>L6</u> |
| <u>L5</u> | (712/2-300)[CCLS] | 12448 | <u>L5</u> |
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| <u>L4</u> | L3 and (way\$1 or flow\$3 or direction\$1) | 274 | <u>L4</u> |
| <u>L3</u> | L2 and (taken or pattern\$1 or like\$7 or possib\$7) | 277 | <u>L3</u> |
| <u>L2</u> | L1 and physical near4 address\$4 and virtual near5 address\$4 | 277 | <u>L2</u> |
| L1 | branch\$3 near8 predict\$4 and histor\$4 | 1962 | L1 |

END OF SEARCH HISTORY



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| | | ŗ | 3. | Branch prediction Gummaraju, J.; Fr. Paraliel Architectur 15-19 Oct 2000 P Digital Object Iden | anklin, M.; res.and Compilat age(s):179 - 188 | ion Techniques, 20 | XVI. Proceedings. | .international.C | Conference o |
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| | | D | 4. | Design optimizati Chen, IC.K.; Chili Computer Design: Conference on 12-15 Oct. 1997 P. Digital Object Iden | h-Chieh Lee; Pos VLSI in Compute age(s):88 - 96 | tiff, M.; Mudge, T.; ers and Processors | · | | 1997 IEEE I |
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5. The effects of mispredicted-path execution on branch prediction structures

Jourdan, S.; Tse-Hao Hsing; Stark, J.; Patt, Y.N.;

Parallel Architectures and Compilation Techniques, 1996. Proceedings of the 1996 Conference on 20-23 Oct. 1996 Page(s):58 - 67

Digital Object Identifier 10.1109/PACT.1996.552555

AbstractPlus | Full Text: PDF(964 KB) IEEE CNF Rights and Permissions 6. Exploring design space of scalable per-address branch predictors Kongmunvattana, A.; Tiamkaew, E.; TENCON 2004, 2004 IEEE Region 10 Conference Volume B, 21-24 Nov. 2004 Page(s):156 - 159 Vol. 2 Digital Object Identifier 10.1109/TENCON.2004.1414555 AbstractPlus | Full Text: PDF(1848 KB) IEEE CNF Rights and Permissions 7. Branch behavior of a commercial OLTP workload on Intel IA32 processors Annavaram, M.; Trung Diep; Shen, J.; Computer Design: VLSI in Computers and Processors, 2002, Proceedings, 2002 IEEE International 16-18 Sept. 2002 Page(s):242 - 248 Digital Object Identifier 10.1109/ICCD.2002.1106777 AbstractPlus | Full Text: PDE(422 KB) IIIEE CNE Rights and Permissions 8. Alloyed path-pattern scheme for branch prediction ___ Ramanujam, R.; Ravirala, M.; Lee, G.; Computer Design, 2001, ICCD 2001, Proceedings, 2001 International Conference on 23-26 Sept. 2001 Page(s):534 - 537 Digital Object Identifier 10.1109/ICCD.2001.955086 AbstractPlus | Full Text: PDE(344 KB) | KEEL CNF Rights and Permissions 9. Effective ahead pipelining of instruction block address generation Seznec, A.; Fraboulet, A.; Computer Architecture, 2003, Proceedings, 30th Annual International Symposium on 9-11 June 2003 Page(s):241 - 252 Digital Object Identifier 10.1109/ISCA.2003.1207004 AbstractPlus | Full Text: PDF(367 KB) IEEE CNF Rights and Permissions 10. Branch history table indexing to prevent pipeline bubbles in wide-issue superscalar proces: Tse-Yu Yeh; Patt, Y.N.; Microarchitecture, 1993. Proceedings of the 26th Annual International Symposium on 1-3 Dec. 1993 Page(s):164 - 175 Digital Object Identifier 10.1109/MICRO.1993.282746 AbstractPlus | Full Text: PDF(972 KB) ISEE CNF Rights and Permissions 11. Extending the PPM branch predictor da Silva, Z.C.; Martini, J.A.; Goncalves, R.A.L.; Parallel Distributed and Network-Based Processing, 2006, PDP 2006, 14th Euromicro Internation; 15-17 Feb. 2006 Page(s):4 pp. Digital Object Identifier 10.1109/PDP.2006.36 AbstractPlus | Full Text: PDF(144 KB) ISEE CNF Rights and Permissions 12. Evaluating branch prediction using two-level perceptron table Ribas, L.V.M.; Goncalves, R.A.L.; Parallel, Distributed, and Network-Based Processing, 2006, PDP 2006, 14th Euromicro Internation: 15-17 Feb. 2006 Page(s):4 pp. Digital Object Identifier 10.1109/PDP.2006.34 AbstractPlus | Full Text: PDF(152 KB) IMEE CNF Rights and Permissions

| 3 3 | Sakamoto, M.; Nunomura, Y.; Yoshida, T.; Shimazu, Y.; Computer Design: VLSI in Computers and Processors, 1996, ICCD '96, Proceedings., 1996 IEEE I Conference on 7-9 Oct. 1996 Page(s):208 - 216 Digital Object Identifier 10.1109/ICCD.1996.563559 AbstractPlus Full Text: PDE(776 KB) IEEE CNF |
|------------|---|
| | Rights and Permissions |
| | 14. Influence of high-level program structures on branch prediction accuracy Ganjoo, A.; Nian-Feng Tzeng; Euromicro Conference, 2000, Proceedings of the 26th Volume 1, 5-7 Sept. 2000 Page(s):316 - 322 vol.1 Digital Object Identifier 10.1109/EURMIC.2000.874648 |
| | AbstractPlus Full Text: PDE(540 KB) ISES CNF Rights and Permissions |
| | 15. TCP: tag correlating prefetchers Hu, Z.; Martonosi, M.; Kaxiras, S.; High-Performance Computer Architecture. 2003. HPCA-9 2003. Proceedings. The Ninth Internation 8-12 Feb. 2003 Page(s):317 - 326 Digital Object Identifier 10.1109/HPCA.2003.1183549 |
| | AbstractPlus Full Text: PDF(348 KB) ISSE CRF Rights and Permissions |
| | 16. An X86 microprocessor with multimedia extensions Draper, D.A.; Crowley, M.P.; Holst, J.; Favor, G.; Schoy, A.; Ben-Meir, A.; Trull, J.; Khanna, R.; We Nolan, J.; Partovi, H.; Johnson, M.; Lee, T.; Mallick, D.; Frydel, G.; Vuong, A.; Yu, S.; Maley, R.; Ka Solid-State Circuits Conference, 1997. Digest of Technical Papers, 44th ISSCC, 1997 IEEE Interns 6-8 Feb. 1997. Page(s):172 - 173, 450 Digital Object Identifier 10.1109/ISSCC.1997.585321 AbstractPlus Full Text: PDE(872 KB) IEEE CNF |
| | Rights and Permissions |
| | 17. Access region locality for high-bandwidth processor memory system design Sangyeun Cho; Pen-Chung Yew; Gyungho Lee; Microarchitecture, 1999 MICRO-32 Proceedings 32nd Annual International Symposium on 16-18 Nov. 1999 Page(s):136 - 146 Digital Object Identifier 10.1109/MICRO.1999.809451 AbstractPlus Full Text: PDE(592 KB) III EE CNIII |
| | Rights and Permissions |
| | 18. Accelerated life testing of marine cables and connectors: problems, pitfalls, and suggestion Ramotowski, T.; Oceans '02 MTS/iEEE Volume 1, 29-31 Oct. 2002 Page(s):512 - 516 vol.1 Digital Object Identifier 10.1109/OCEANS.2002.1193321 |
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